

# The BalusC Server

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## K7

Vragen of informatie over een *processor*?  
 Vragen over de keuze van een *processor*?  
 Vragen over het samenstellen van een *PC*?  
 Discussieren over (toekomstige) *hardware*?  
 Andere *PC-related* **discussies** of **vragen**?

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The BalusC Forum

## Athlon (SlotA)

Implementation Argon/Pluto/Thunderbird/Orion: 575pins BGA - 242pins SEC - SlotA

Instruction set Argon/Pluto/Thunderbird/Orion: RISC - IA32 - MMX - Ext. 3DNow!

[\[info\]](#) AMD Athlon Processor Tech Docs

CPU info			settings			cache			elec (max)			physics		
name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Athlon 500</b>														
<i>Argon</i>														
C1 (6-1-1)	08/1999	500MHz	100MHz	5.0x	1.6V	L1 128KB				27.4A			22.0M	
C2 (6-1-2)	10/1999		DDR			L2 512KB				42.0W			250nm	
OEM: K7500MTR51B										70.0°C			184mm <sup>2</sup>	
<b>Athlon 550</b>														
<i>Argon</i>														
C1 (6-1-1)	08/1999	550MHz	100MHz	5.5x	1.6V	L1 128KB				30.1A			22.0M	
C2 (6-1-2)	10/1999		DDR			L2 512KB				46.0W			250nm	
OEM: K7550MTR51B										70.0°C			184mm <sup>2</sup>	
<b>Athlon 550</b>														
<i>Pluto (K75)</i>														
A1 (6-2-1)	01/2000	550MHz	100MHz	5.5x	1.6V	L1 128KB				20.0A			22.0M	
A2 (6-2-2)	03/2000		DDR			L2 512KB				31.0W			180nm	
OEM: K7550MTR51B										70.0°C			102mm <sup>2</sup>	
<b>Athlon 600</b>														
<i>Argon</i>														
C1 (6-1-1)	08/1999	600MHz	100MHz	6.0x	1.6V	L1 128KB				32.9A			22.0M	
C2 (6-1-2)	10/1999		DDR			L2 512KB				50.0W			250nm	
OEM: K7600MTR51B										70.0°C			184mm <sup>2</sup>	
<b>Athlon 600</b>														
<i>Pluto (K75)</i>														
A1 (6-2-1)	01/2000	600MHz	100MHz	6.0x	1.6V	L1 128KB				21.5A			22.0M	
A2 (6-2-2)	03/2000		DDR			L2 512KB				34.0W			180nm	
OEM: K7600MTR51B										70.0°C			102mm <sup>2</sup>	
<b>Athlon 650</b>														
<i>Thunderbird</i>														
A4 (6-4-2)	06/2000	650MHz	100MHz	6.5x	1.7V	L1 128KB				23.8A			37.0M	
A9 (6-4-4)	06/2000		DDR			L2 256KB				36.1W			180nm	
OEM: A0650MPR24B										70.0°C			120mm <sup>2</sup>	
<b>Athlon 650</b>														
<i>Argon</i>														
C1 (6-1-1)	08/1999	650MHz	100MHz	6.5x	1.6V	L1 128KB				35.5A			22.0M	
C2 (6-1-2)	10/1999		DDR			L2 512KB				54.0W			250nm	
OEM: K7650MTR51B										70.0°C			184mm <sup>2</sup>	
<b>Athlon 650</b>														
<i>Pluto (K75)</i>														
A1 (6-2-1)	01/2000	650MHz	100MHz	6.5x	1.6V	L1 128KB				22.9A			22.0M	
A2 (6-2-2)	03/2000		DDR			L2 512KB				36.0W			180nm	
OEM: K7650MTR51B										70.0°C			102mm <sup>2</sup>	
<b>Athlon 700</b>														
<i>Thunderbird</i>														
A4 (6-4-2)	06/2000	700MHz	100MHz	7.0x	1.7V	L1 128KB				25.2A			37.0M	
A9 (6-4-4)	06/2000		DDR			L2 256KB				38.3W			180nm	
OEM: A0700MPR24B										70.0°C			120mm <sup>2</sup>	
<b>Athlon 700</b>														
<i>Argon</i>														
C2 (6-1-2)	10/1999	700MHz	100MHz	7.0x	1.6V	L1 128KB				33.1A			22.0M	
OEM: K7700MTR51B										70.0°C			250nm	
<b>Athlon 700</b>														
<i>Pluto (K75)</i>														
A1 (6-2-1)	01/2000	700MHz	100MHz	7.0x	1.6V	L1 128KB				24.4A			22.0M	
A2 (6-2-2)	03/2000		DDR			L2 512KB				39.0W			180nm	
OEM: K7700MTR51B										70.0°C			102mm <sup>2</sup>	
<b>Athlon 750</b>														
<i>Thunderbird</i>														
A4 (6-4-2)	06/2000	750MHz	100MHz	7.5x	1.7V	L1 128KB				26.6A			37.0M	
A9 (6-4-4)	06/2000		DDR			L2 256KB				40.4W			180nm	
OEM: A0750MPR24B										70.0°C			120mm <sup>2</sup>	
<b>Athlon 750</b>														
<i>Pluto (K75)</i>														
			100MHz			L1 128KB				25.8A			22.0M	

A1 (6-2-1) 11/1999			<i>DDR</i>			L2 512KB	40.0W	180nm
A2 (6-2-2) 03/2000	750MHz			7.5x	1.6V		70.0°C	102mm <sup>2</sup>
OEM: K7750MTR52B								
<b>Athlon 800</b>								
<i>Thunderbird</i>								
A4 (6-4-2) 06/2000	800MHz	100MHz		8.0x	1.7V	L1 128KB	28.0A	37.0M
A9 (6-4-4) 06/2000		<i>DDR</i>				L2 256KB	42.6W	180nm
OEM: A0800MPR24B								
<b>Athlon 800</b>								
<i>Pluto (K75)</i>								
A1 (6-2-1) 01/2000	800MHz	100MHz		8.0x	1.7V	L1 128KB	29.5A	22.0M
A2 (6-2-2) 03/2000		<i>DDR</i>				L2 512KB	48.0W	180nm
OEM: K7800MTR52B								
<b>Athlon 850</b>								
<i>Thunderbird</i>								
A4 (6-4-2) 06/2000	850MHz	100MHz		8.5x	1.7V	L1 128KB	29.4A	37.0M
A9 (6-4-4) 06/2000		<i>DDR</i>				L2 256KB	44.8W	180nm
OEM: A0850MPR24B								
<b>Athlon 850</b>								
<i>Pluto (K75)</i>								
A1 (6-2-1) 02/2000	850MHz	100MHz		8.5x	1.7V	L1 128KB	30.0A	22.0M
A2 (6-2-2) 03/2000		<i>DDR</i>				L2 512KB	50.0W	180nm
OEM: K7850MTR52B								
<b>Athlon 900</b>								
<i>Thunderbird</i>								
A9 (6-4-4) 06/2000	900MHz	100MHz		9.0x	1.75V	L1 128KB	31.7A	37.0M
OEM: A0900MMR24B								
<b>Athlon 900</b>								
<i>Pluto (K75)</i>								
A2 (6-2-2) 03/2000	900MHz	100MHz		9.0x	1.8V	L1 128KB	34.0A	22.0M
OEM: K7900MNR53B								
<b>Athlon 950</b>								
<i>Thunderbird</i>								
A9 (6-4-4) 06/2000	950MHz	100MHz		9.5x	1.75V	L1 128KB	33.2A	37.0M
OEM: A0950MMR24B								
<b>Athlon 950</b>								
<i>Pluto (K75)</i>								
A2 (6-2-2) 03/2000	950MHz	100MHz		9.5x	1.8V	L1 128KB	35.0A	22.0M
OEM: K7950MNR53B								
<b>Athlon 1000</b>								
<i>Thunderbird</i>								
A9 (6-4-4) 06/2000	1000MHz	100MHz		10.0x	1.75V	L1 128KB	34.7A	37.0M
OEM: A1000MMR24B								
<b>Athlon 1000</b>								
<i>Orion (K75)</i>								
A2 (6-2-2) 03/2000	1000MHz	100MHz		10.0x	1.8V	L1 128KB	37.0A	22.0M
OEM: K7100MNR53B								

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## Duron (SocketA)

Implementation Spitfire: 462pins CPGA - SocketA

Implementation Morgan/Applebred: 472pins OPGA - SocketA

Instruction set Spitfire: RISC - IA32 - MMX - Ext. 3DNow!

Instruction set Morgen/Applebred: RISC - IA32 - MMX - Ext. 3DNow! - SSE

[\[info\]](#) AMD Duron Processor Tech Docs

CPU info			settings			cache			elec (max)			physics		
name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Duron 600</b>														
<i>Spitfire</i>														
A0 (6-3-0) 06/2000		600MHz	100MHz	6.0x	1.6V	L1 128KB				17.1A			25.0M	
A2 (6-3-1) 01/2001			<i>DDR</i>			L2 64KB				27.4W			180nm	
OEM: D600AUT1B														
<b>Duron 650</b>														
<i>Spitfire</i>														
A0 (6-3-0) 06/2000		650MHz	100MHz	6.5x	1.6V	L1 128KB				18.4A			25.0M	
A2 (6-3-1) 01/2001			<i>DDR</i>			L2 64KB				29.4W			180nm	
OEM: D650AUT1B														
<b>Duron 700</b>														
<i>Spitfire</i>														
A0 (6-3-0) 06/2000		700MHz	100MHz	7.0x	1.6V	L1 128KB				19.6A			25.0M	
A2 (6-3-1) 01/2001			<i>DDR</i>			L2 64KB				31.4W			180nm	
OEM: D700AUT1B														
<b>Duron 750</b>														
<i>Spitfire</i>														
A0 (6-3-0) 09/2000		750MHz	100MHz	7.5x	1.6V	L1 128KB				20.9A			25.0M	
A2 (6-3-1) 01/2001			<i>DDR</i>			L2 64KB				33.4W			180nm	
OEM: D750AUT1B														
<b>Duron 800</b>														
<i>Spitfire</i>														
A0 (6-3-0) 10/2000		800MHz	100MHz	8.0x	1.6V	L1 128KB				22.1A			25.0M	
A2 (6-3-1) 01/2001			<i>DDR</i>			L2 64KB				35.4W			180nm	
OEM: D800AUT1B														

<b>Duron 850</b> <i>Spitfire</i> A2 (6-3-1) 01/2001 OEM: D850AUT1B	850MHz	100MHz <i>DDR</i>	8.5x	1.6V	L1 128KB L2 64KB	23.4A 37.4W 90.0°C	25.0M 180nm 100mm <sup>2</sup>
<b>Duron 900</b> <i>Spitfire</i> A2 (6-3-1) 04/2001 OEM: D900AUT1B	900MHz	100MHz <i>DDR</i>	9.0x	1.6V	L1 128KB L2 64KB	24.7A 39.5W 90.0°C	25.0M 180nm 100mm <sup>2</sup>
<b>Duron 900</b> <i>Morgan</i> A0 (6-7-0) 07/2001 A1 (6-7-1) 01/2002 OEM: DHD900AMT1B	900MHz	100MHz <i>DDR</i>	9.0x	1.75V	L1 128KB L2 64KB	24.4A 42.7W 90.0°C	25.2M 180nm 106mm <sup>2</sup>
<b>Duron 950</b> <i>Spitfire</i> A2 (6-3-1) 06/2001 OEM: D950AUT1B	950MHz	100MHz <i>DDR</i>	9.5x	1.6V	L1 128KB L2 64KB	25.9A 41.5W 90.0°C	25.0M 180nm 100mm <sup>2</sup>
<b>Duron 950</b> <i>Morgan</i> A0 (6-7-0) 07/2001 A1 (6-7-1) 01/2002 OEM: DHD950AMT1B	950MHz	100MHz <i>DDR</i>	9.5x	1.75V	L1 128KB L2 64KB	25.4A 44.4W 90.0°C	25.2M 180nm 106mm <sup>2</sup>
<b>Duron 1000</b> <i>Morgan</i> A0 (6-7-0) 07/2001 A1 (6-7-1) 01/2002 OEM: DHD1000AMT1B	1000MHz	100MHz <i>DDR</i>	10.0x	1.75V	L1 128KB L2 64KB	26.3A 46.1W 90.0°C	25.2M 180nm 106mm <sup>2</sup>
<b>Duron 1100</b> <i>Morgan</i> A0 (6-7-0) 07/2001 A1 (6-7-1) 01/2002 OEM: DHD1100AMT1B	1100MHz	100MHz <i>DDR</i>	11.0x	1.75V	L1 128KB L2 64KB	28.7A 50.3W 90.0°C	25.2M 180nm 106mm <sup>2</sup>
<b>Duron 1200</b> <i>Morgan</i> A0 (6-7-0) 07/2001 A1 (6-7-1) 01/2002 OEM: DHD1200AMT1B	1200MHz	100MHz <i>DDR</i>	12.0x	1.75V	L1 128KB L2 64KB	31.3A 54.7W 90.0°C	25.2M 180nm 106mm <sup>2</sup>
<b>Duron 1300</b> <i>Morgan</i> A1 (6-7-1) 01/2002 OEM: DHD1300AMT1B	1300MHz	100MHz <i>DDR</i>	13.0x	1.75V	L1 128KB L2 64KB	34.3A 60.0W 90.0°C	25.2M 180nm 106mm <sup>2</sup>
<b>Duron 1400</b> <i>Applebred</i> B0 (6-8-1) 08/2003 OEM: DHD1400DLV1C	1400MHz	133MHz <i>DDR</i>	10.5x	1.5V	L1 128KB L2 64KB	38.0A 57.0W 85.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Duron 1600</b> <i>Applebred</i> B0 (6-8-1) 08/2003 OEM: DHD1600DLV1C	1600MHz	133MHz <i>DDR</i>	12.0x	1.5V	L1 128KB L2 64KB	38.0A 57.0W 85.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Duron 1800</b> <i>Applebred</i> B0 (6-8-1) 08/2003 OEM: DHD1800DLV1C	1800MHz	133MHz <i>DDR</i>	13.5x	1.5V	L1 128KB L2 64KB	38.0A 57.0W 85.0°C	37.2M 130nm 85mm <sup>2</sup>

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## Athlon (SocketA)

Implementation Thunderbird: 462pins CPGA - SocketA  
 Instruction set Thunderbird: RISC - IA32 - MMX - Ext. 3DNow!  
[\[info\]](#) AMD Athlon Processor Tech Docs

CPU info	settings			cache			elec (max)			physics					
	name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Athlon 700</b> <i>Thunderbird</i> A4 (6-4-2) 05/2000 A9 (6-4-4) 08/2000 OEM: A0700APT3B (1, 70V) OEM: A0700AMT3B (1, 75V)			700MHz	100MHz <i>DDR</i>	7.0x	1.7/1.75V	L1 128KB L2 256KB				25.2/23.0A 38.3/40.0W 90.0°C		37.2M 130nm 120mm <sup>2</sup>		
<b>Athlon 750</b> <i>Thunderbird</i> A4 (6-4-2) 05/2000 A9 (6-4-4) 08/2000 OEM: A0750APT3B (1, 70V) OEM: A0750AMT3B (1, 75V)			750MHz	100MHz <i>DDR</i>	7.5x	1.7/1.75V	L1 128KB L2 256KB				26.6/25.0A 40.4/43.0W 90.0°C		37.2M 130nm 120mm <sup>2</sup>		
<b>Athlon 800</b> <i>Thunderbird</i> A4 (6-4-2) 05/2000 A9 (6-4-4) 08/2000 OEM: A0800APT3B (1, 70V) OEM: A0800AMT3B (1, 75V)			800MHz	100MHz <i>DDR</i>	8.0x	1.7/1.75V	L1 128KB L2 256KB				28.0/26.0A 42.6/45.0W 90.0°C		37.0M 180nm 120mm <sup>2</sup>		
<b>Athlon 850</b> <i>Thunderbird</i> A4 (6-4-2) 05/2000 A9 (6-4-4) 08/2000			850MHz	100MHz <i>DDR</i>	8.5x	1.7/1.75V	L1 128KB L2 256KB				29.4/27.0A 44.8/47.0W 90.0°C		37.0M 180nm 120mm <sup>2</sup>		

OEM: A085OAPT3B (1, 70V)  
 OEM: A085OAMT3B (1, 75V)

<b>Athlon 900</b> <i>Thunderbird</i> A9 (6-4-4) 08/2000 OEM: A090OAMT3B	900MHz	100MHz <i>DDR</i>	9.0x	1.75V	L1 128KB L2 256KB	29.0A 50.0W 90.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 950</b> <i>Thunderbird</i> A9 (6-4-4) 08/2000 OEM: A095OAMT3B	950MHz	100MHz <i>DDR</i>	9.5x	1.75V	L1 128KB L2 256KB	30.0A 52.0W 90.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1000 (200)</b> <i>Thunderbird</i> A9 (6-4-4) 08/2000 OEM: A100OAMT3B	1000MHz	100MHz <i>DDR</i>	10.0x	1.75V	L1 128KB L2 256KB	34.6A 54.3W 90.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1000 (266)</b> <i>Thunderbird</i> A9 (6-4-4) 10/2000 OEM: A100OAMT3C	1000MHz	133MHz <i>DDR</i>	7.5x	1.75V	L1 128KB L2 256KB	31.0A 54.0W 90.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1100</b> <i>Thunderbird</i> A9 (6-4-4) 08/2000 OEM: A110OAMS3B	1100MHz	100MHz <i>DDR</i>	11.0x	1.75V	L1 128KB L2 256KB	34.0A 60.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1133</b> <i>Thunderbird</i> A9 (6-4-4) 10/2000 OEM: A1133AMS3C	1133MHz	133MHz <i>DDR</i>	8.5x	1.75V	L1 128KB L2 256KB	36.0A 63.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1200 (200)</b> <i>Thunderbird</i> A9 (6-4-4) 10/2000 OEM: A120OAMS3B	1200MHz	100MHz <i>DDR</i>	12.0x	1.75V	L1 128KB L2 256KB	38.0A 66.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1200 (266)</b> <i>Thunderbird</i> A9 (6-4-4) 10/2000 OEM: A120OAMS3C	1200MHz	133MHz <i>DDR</i>	9.0x	1.75V	L1 128KB L2 256KB	38.0A 66.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1300</b> <i>Thunderbird</i> A9 (6-4-4) 03/2001 OEM: A130OAMS3B	1300MHz	100MHz <i>DDR</i>	13.0x	1.75V	L1 128KB L2 256KB	39.0A 68.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1333</b> <i>Thunderbird</i> A9 (6-4-4) 03/2001 OEM: A1333AMS3C	1333MHz	133MHz <i>DDR</i>	10.0x	1.75V	L1 128KB L2 256KB	40.0A 70.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1400 (200)</b> <i>Thunderbird</i> A9 (6-4-4) 06/2001 OEM: A140OAMS3B	1400MHz	100MHz <i>DDR</i>	14.0x	1.75V	L1 128KB L2 256KB	42.0A 72.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>
<b>Athlon 1400 (266)</b> <i>Thunderbird</i> A9 (6-4-4) 06/2001 OEM: A140OAMS3C	1400MHz	133MHz <i>DDR</i>	10.5x	1.75V	L1 128KB L2 256KB	42.0A 72.0W 95.0°C	37.0M 180nm 120mm <sup>2</sup>

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## Athlon XP (SocketA)

Implementation Palomino/Thoroughbred/Thornton/Barton: 462pins OPGA - SocketA

Instruction set Palomino/Thoroughbred/Thornton/Barton: RISC - IA32 - MMX - Ext. 3DNow! - SSE

[\[info\]](#) AMD Athlon XP Processor Tech Docs

CPU info	settings					cache			elec (max)			physics			
	name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Athlon XP1500+</b> <i>Palomino</i> A0 (6-6-0) 09/2001 A2 (6-6-1) 11/2001 A5 (6-6-2) 03/2002 OEM: AX150ODMT3C			1333MHz	133MHz <i>DDR</i>	10.0x	1.75V	L1 128KB L2 256KB				34.3A 60.0W 90.0°C				37.5M 180nm 130mm <sup>2</sup>
<b>Athlon XP1600+</b> <i>Palomino</i> A0 (6-6-0) 09/2001 A2 (6-6-1) 11/2001 A5 (6-6-2) 03/2002 OEM: AX160ODMT3C			1400MHz	133MHz <i>DDR</i>	10.5x	1.75V	L1 128KB L2 256KB				35.9A 62.8W 90.0°C				37.5M 180nm 130mm <sup>2</sup>
<b>Athlon XP1600+</b> <i>Thoroughbred</i> B0 (6-8-1) 12/2002 OEM: AXDA160ODUT3C			1400MHz	133MHz <i>DDR</i>	10.5x	1.6V	L1 128KB L2 256KB				30.3A 48.5W 90.0°C				37.2M 130nm 85mm <sup>2</sup>
<b>Athlon XP1700+</b> <i>Palomino</i> A0 (6-6-0) 09/2001 A2 (6-6-1) 11/2001 A5 (6-6-2) 03/2002 OEM: AX170ODMT3C			1466MHz	133MHz <i>DDR</i>	11.0x	1.75V	L1 128KB L2 256KB				36.6A 64.0W 90.0°C				37.5M 180nm 130mm <sup>2</sup>
<b>Athlon XP1700+</b> <i>Thoroughbred</i>											32.9/30.9A				37.2M

AO (6-8-0) 06/2002 BO (6-8-1) 12/2002 OEM: AXDA1700DLT3C (AO) OEM: AXDA1700DUT3C (BO)	1466MHz	133MHz DDR	11.0x	1.5/1.6V	L1 128KB L2 256KB	49.4W 90.0°C	130nm 81/85mm <sup>2</sup>
<b>Athlon XP1800+</b> <i>Palomino</i> AO (6-6-0) 09/2001 A2 (6-6-1) 11/2001 A5 (6-6-2) 03/2002 OEM: AX1800DMT3C	1533MHz	133MHz DDR	11.5x	1.75V	L1 128KB L2 256KB	37.7A 66.0W 90.0°C	37.5M 180nm 130mm <sup>2</sup>
<b>Athlon XP1800+</b> <i>Thoroughbred</i> AO (6-8-0) 06/2002 BO (6-8-1) 12/2002 OEM: AXDA1800DLT3C (AO) OEM: AXDA1800DUT3C (BO)	1533MHz	133MHz DDR	11.5x	1.5/1.6V	L1 128KB L2 256KB	34.0/31.9A 51.0W 90.0°C	37.2M 130nm 81/85mm <sup>2</sup>
<b>Athlon XP1900+</b> <i>Palomino</i> A2 (6-6-1) 11/2001 A5 (6-6-2) 03/2002 OEM: AX1900DMT3C	1600MHz	133MHz DDR	12.0x	1.75V	L1 128KB L2 256KB	38.9A 68.0W 90.0°C	37.5M 180nm 130mm <sup>2</sup>
<b>Athlon XP1900+</b> <i>Thoroughbred</i> AO (6-8-0) 06/2002 BO (6-8-1) 12/2002 OEM: AXDA1900DLT3C (AO) OEM: AXDA1900DUT3C (BO)	1600MHz	133MHz DDR	12.0x	1.5/1.6V	L1 128KB L2 256KB	35.0/32.8A 52.5W 90.0°C	37.2M 130nm 81/85mm <sup>2</sup>
<b>Athlon XP2000+</b> <i>Palomino</i> A2 (6-6-1) 01/2002 A5 (6-6-2) 03/2002 OEM: AX2000DMT3C	1666MHz	133MHz DDR	12.5x	1.75V	L1 128KB L2 256KB	40.0A 70.0W 90.0°C	37.5M 180nm 130mm <sup>2</sup>
<b>Athlon XP2000+</b> <i>Thoroughbred</i> AO (6-8-0) 06/2002 BO (6-8-1) 12/2002 OEM: AXDA2000DUT3C (AO) OEM: AXDA2000DKT3C (BO)	1666MHz	133MHz DDR	12.5x	1.6/1.65V	L1 128KB L2 256KB	37.7/36.5A 60.3W 90.0°C	37.2M 130nm 81/85mm <sup>2</sup>
<b>Athlon XP2000+</b> <i>Thorton</i> A2 (6-A-0) 09/2003 OEM: AXDC2000DUT3C (1, 60V) OEM: AXDC2000DLT3C (1, 50V)	1666MHz	133MHz DDR	12.5x	1.6/1.5V	L1 128KB L2 256KB	37.7A 60.3W 90.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2100+</b> <i>Palomino</i> A5 (6-6-2) 03/2002 OEM: AX2100DMT3C	1733MHz	133MHz DDR	13.0x	1.75V	L1 128KB L2 256KB	41.1A 72.0W 90.0°C	37.5M 180nm 130mm <sup>2</sup>
<b>Athlon XP2100+</b> <i>Thoroughbred</i> AO (6-8-0) 06/2002 BO (6-8-1) 12/2002 OEM: AXDA2100DUT3C	1733MHz	133MHz DDR	13.0x	1.6V	L1 128KB L2 256KB	38.8A 62.1W 90.0°C	37.2M 130nm 81/85mm <sup>2</sup>
<b>Athlon XP2200+</b> <i>Thoroughbred</i> AO (6-8-0) 06/2002 BO (6-8-1) 12/2002 OEM: AXDA2200DKV3C (AO) OEM: AXDA2200DUV3C (BO)	1800MHz	133MHz DDR	13.5x	1.65/1.6V	L1 128KB L2 256KB	41.2/39.3A 67.9/62.8W 85.0°C	37.2M 130nm 81/85mm <sup>2</sup>
<b>Athlon XP2200+</b> <i>Thorton</i> A2 (6-A-0) 09/2003 OEM: AXDC2200DUV3C (1, 60V) OEM: AXDC2200DLV3C (1, 50V)	1800MHz	133MHz DDR	13.5x	1.6/1.5V	L1 128KB L2 256KB	39.3A 62.8W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2400+</b> <i>Thoroughbred</i> BO (6-8-1) 08/2002 OEM: AXDA2400DKV3C	2000MHz	133MHz DDR	15.0x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Athlon XP2400+</b> <i>Thorton</i> A2 (6-A-0) 09/2003 OEM: AXDC2400DKV3C	2000MHz	133MHz DDR	15.0x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2500+ (266)</b> <i>Barton</i> A2 (6-A-0) 12/2004 OEM: AXDA2500DKV4C	1867MHz	133MHz DDR	14.0x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2500+ (333)</b> <i>Barton</i> A2 (6-A-0) 02/2003 OEM: AXDA2500DKV4D	1833MHz	166MHz DDR	11.0x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2600+ (266)</b> <i>Barton</i> A2 (6-A-0) 12/2004 OEM: AXDA2600DKV4C	2000MHz	133MHz DDR	15.0x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2600+ (266)</b> <i>Thoroughbred</i> BO (6-8-1) 08/2002 OEM: AXDA2600DKV3C	2133MHz	133MHz DDR	16.0x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Athlon XP2600+ (266)</b>							

<i>Thorton</i> A2 (6-A-0) 09/2003 OEM: AXDC2600DKV3C	2133MHz	133MHz <i>DDR</i>	16.0x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2600+</b> (333) <i>Barton</i> A2 (6-A-0) 02/2003 OEM: AXDA2600DKV4D	1917MHz	166MHz <i>DDR</i>	11.5x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2600+</b> (333) <i>Thoroughbred</i> BO (6-8-1) 12/2002 OEM: AXDA2600DKV3D	2083MHz	166MHz <i>DDR</i>	12.5x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Athlon XP2600+</b> (333) <i>Thorton</i> A2 (6-A-0) 09/2004 OEM: AXDC2600DKV3D	2083MHz	166MHz <i>DDR</i>	12.5x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2700+</b> <i>Thoroughbred</i> BO (6-8-1) 12/2002 OEM: AXDA2700DKV3D	2166MHz	166MHz <i>DDR</i>	13.0x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Athlon XP2800+</b> <i>Thoroughbred</i> BO (6-8-1) 12/2002 OEM: AXDA2800DKV3D	2250MHz	166MHz <i>DDR</i>	13.5x	1.65V	L1 128KB L2 256KB	45.0A 74.3W 85.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Athlon XP2800+</b> (266) <i>Barton</i> A2 (6-A-0) 12/2004 OEM: AXDA2800DKV4C	2083MHz	133MHz <i>DDR</i>	16.0x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2800+</b> (333) <i>Barton</i> A2 (6-A-0) 02/2003 OEM: AXDA2800DKV4D	2083MHz	166MHz <i>DDR</i>	12.5x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP2900+</b> <i>Barton</i> A2 (6-A-0) 12/2004 OEM: AXDA2900DKV4E	2000MHz	200MHz <i>DDR</i>	10.0x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP3000+</b> (333) <i>Barton</i> A2 (6-A-0) 02/2003 OEM: AXDA3000DKV4D	2166MHz	166MHz <i>DDR</i>	13.0x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP3000+</b> (400) <i>Barton</i> A2 (6-A-0) 05/2003 OEM: AXDA3000DKV4E	2100MHz	200MHz <i>DDR</i>	10.5x	1.65V	L1 128KB L2 512KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP3100+</b> <i>Thorton</i> A2 (6-A-0) 12/2003 OEM: AXDC3100DKV3E	2200MHz	200MHz <i>DDR</i>	11.0x	1.65V	L1 128KB L2 256KB	41.4A 68.3W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP3200+</b> (333) <i>Barton</i> A2 (6-A-0) 12/2004 OEM: AXDA3200DKV4D	2333MHz	166MHz <i>DDR</i>	14.0x	1.65V	L1 128KB L2 512KB	46.5A 76.8W 85.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon XP3200+</b> (400) <i>Barton</i> A2 (6-A-0) 05/2003 OEM: AXDA3200DKV4E	2200MHz	200MHz <i>DDR</i>	11.0x	1.65V	L1 128KB L2 512KB	46.5A 76.8W 85.0°C	54.3M 130nm 101mm <sup>2</sup>

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## Sempron (SocketA)

Implementation Thoroughbred/Thorton/Barton: 462pins OPGA - SocketA

Instruction set Thoroughbred/Thorton/Barton: RISC - IA32 - MMX - Ext. 3DNow! - SSE

[\[info\]](#) AMD Sempron Processor Tech Docs

CPU info		settings				cache			elec (max)			physics		
name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Sempron 2200+</b> <i>Thoroughbred</i> BO (6-8-1) 07/2004 OEM: SDA2200DUT3D		1500MHz	166MHz <i>DDR</i>	9.0x	1.6V	L1 128KB L2 256KB				38.75A 62.0W 90.0°C			37.2M 130nm 85mm <sup>2</sup>	
<b>Sempron 2200+</b> <i>Thorton</i> A2 (6-A-0) 08/2004 OEM: SDC2200DUT3D		1500MHz	166MHz <i>DDR</i>	9.0x	1.6V	L1 128KB L2 256KB				38.75A 62.0W 90.0°C			54.3M 130nm 101mm <sup>2</sup>	
<b>Sempron 2300+</b> <i>Thoroughbred</i> BO (6-8-1) 07/2004 OEM: SDA2300DUT3D		1583MHz	166MHz <i>DDR</i>	9.5x	1.6V	L1 128KB L2 256KB				38.75A 62.0W 90.0°C			37.2M 130nm 85mm <sup>2</sup>	
<b>Sempron 2400+</b> <i>Thoroughbred</i> BO (6-8-1) 07/2004 OEM: SDA2400DUT3D		1666MHz	166MHz <i>DDR</i>	10.0x	1.6V	L1 128KB L2 256KB				38.75A 62.0W 90.0°C			37.2M 130nm 85mm <sup>2</sup>	
<b>Sempron 2500+</b> <i>Thoroughbred</i>		1750MHz	166MHz <i>DDR</i>	10.5x	1.6V	L1 128KB L2 256KB				38.75A 62.0W			37.2M 130nm	

BO (6-8-1) 07/2004 OEM: SDA2500DUT3D							90.0°C	85mm <sup>2</sup>
<b>Sempron 2600+</b> <i>Thoroughbred</i>	1833MHz	166MHz <i>DDR</i>	11.0x	1.6V	L1 128KB L2 256KB		38.75A 62.0W 90.0°C	37.2M 130nm 85mm <sup>2</sup>
BO (6-8-1) 07/2004 OEM: SDA2600DUT3D								
<b>Sempron 2800+</b> <i>Thoroughbred</i>	2000MHz	166MHz <i>DDR</i>	12.0x	1.6V	L1 128KB L2 256KB		38.75A 62.0W 90.0°C	37.2M 130nm 85mm <sup>2</sup>
BO (6-8-1) 07/2004 OEM: SDA2800DUT3D								
<b>Sempron 2800+</b> <i>Thorton</i>	2000MHz	166MHz <i>DDR</i>	12.0x	1.6V	L1 128KB L2 256KB		38.75A 62.0W 90.0°C	54.3M 130nm 101mm <sup>2</sup>
A2 (6-A-0) 08/2004 OEM: SDC2800DUT3D								
<b>Sempron 3000+</b> <i>Barton</i>	2000MHz	166MHz <i>DDR</i>	12.0x	1.6V	L1 128KB L2 512KB		38.75A 62.0W 90.0°C	54.3M 130nm 101mm <sup>2</sup>
A2 (6-A-0) 09/2004 OEM: SDA3000DUT4D								

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## Athlon MP (Multiprocessor SocketA)

Implementation Mustang: 462pins CPGA - SocketA

Implementation Palomino/Thoroughbred/T'bred-B/Barton: 462pins OPGA - SocketA

Instruction set Mustang: RISC - IA32 - MMX - Ext. 3DNow!

Instruction set Palomino/Thoroughbred/T'bred-B/Barton: RISC - IA32 - MMX - Ext. 3DNow! - SSE

[\[info\]](#) AMD Athlon MP Processor Tech Docs

CPU info		settings				cache			elec (max)			physics		
name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Athlon 1000 MP</b>														
<i>Mustang</i>														
A0 (6-6-0) 06/2001		1000MHz	133MHz	7.5x	1.75V	L1 128KB				26.3A			37.5M	
A2 (6-6-1) 11/2001			<i>DDR</i>			L2 256KB				46.1W			180nm	
A5 (6-6-2) 03/2002										95.0°C			130mm <sup>2</sup>	
OEM: AHX1000AMS3C														
<b>Athlon 1200 MP</b>														
<i>Mustang</i>														
A0 (6-6-0) 06/2001		1200MHz	133MHz	9.0x	1.75V	L1 128KB				31.3A			37.5M	
A2 (6-6-1) 11/2001			<i>DDR</i>			L2 256KB				54.7W			180nm	
A5 (6-6-2) 03/2002										95.0°C			130mm <sup>2</sup>	
OEM: AHX1200AMS3C														
<b>Athlon MP1500+</b>														
<i>Palomino</i>														
A0 (6-6-0) 10/2001		1333MHz	133MHz	10.0x	1.75V	L1 128KB				34.3A			37.5M	
A2 (6-6-1) 11/2001			<i>DDR</i>			L2 256KB				60.0W			180nm	
A5 (6-6-2) 03/2002										95.0°C			130mm <sup>2</sup>	
OEM: AMP1500DMS3C														
<b>Athlon MP1600+</b>														
<i>Palomino</i>														
A0 (6-6-0) 10/2001		1400MHz	133MHz	10.5x	1.75V	L1 128KB				35.9A			37.5M	
A2 (6-6-1) 11/2001			<i>DDR</i>			L2 256KB				62.8W			180nm	
A5 (6-6-2) 11/2001										95.0°C			130mm <sup>2</sup>	
OEM: AMP1600DMS3C														
<b>Athlon MP1800+</b>														
<i>Palomino</i>														
A0 (6-6-0) 10/2001		1533MHz	133MHz	11.5x	1.75V	L1 128KB				37.7A			37.5M	
A2 (6-6-1) 11/2001			<i>DDR</i>			L2 256KB				66.0W			180nm	
A5 (6-6-2) 03/2002										95.0°C			130mm <sup>2</sup>	
OEM: AMP1800DMS3C														
<b>Athlon MP1900+</b>														
<i>Palomino</i>														
A2 (6-6-1) 12/2001		1600MHz	133MHz	12.0x	1.75V	L1 128KB				37.7A			37.5M	
A5 (6-6-2) 03/2002			<i>DDR</i>			L2 256KB				66.0W			180nm	
OEM: AMP1900DMS3C														
<b>Athlon MP2000+</b>														
<i>Palomino</i>														
A5 (6-6-2) 03/2002		1666MHz	133MHz	12.5x	1.75V	L1 128KB				37.7A			37.5M	
OEM: AMP2000DMS3C														
<b>Athlon MP2000+</b>														
<i>Thoroughbred</i>														
A0 (6-8-0) 08/2002		1666MHz	133MHz	12.5x	1.6V	L1 128KB				36.4A			37.2M	
BO (6-8-1) 10/2002			<i>DDR</i>			L2 256KB				58.2W			130nm	
OEM: AMSN2000DUT3C														
<b>Athlon MP2100+</b>														
<i>Palomino</i>														
A5 (6-6-2) 06/2002		1733MHz	133MHz	13.0x	1.75V	L1 128KB				37.7A			37.5M	
OEM: AMP2100DMS3C														
<b>Athlon MP2200+</b>														
<i>Thoroughbred</i>														
A0 (6-8-0) 08/2002		1800MHz	133MHz	13.5x	1.65V	L1 128KB				36.4A			37.2M	
BO (6-8-1) 10/2002			<i>DDR</i>			L2 256KB				60.0W			130nm	
OEM: AMSN2200DKT3C														
<b>Athlon MP2400+</b>														

<i>Thoroughbred</i> BO (6-8-1) 12/2002 OEM: AMSN2400DKT3C	2000MHz	133MHz DDR	15.0x	1.65V	L1 128KB L2 256KB	36.4A 60.0W 90.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Athlon MP2600+</b> <i>Barton</i> A2 (6-A-0) 05/2003 OEM: AMSN2600DUT4C	2000MHz	133MHz DDR	15.0x	1.6V	L1 128KB L2 512KB	37.5A 60.0W 90.0°C	54.3M 130nm 101mm <sup>2</sup>
<b>Athlon MP2600+</b> <i>Thoroughbred</i> BO (6-8-1) 02/2003 OEM: AMSN2600DKT3C	2133MHz	133MHz DDR	16.0x	1.65V	L1 128KB L2 256KB	36.4A 60.0W 90.0°C	37.2M 130nm 85mm <sup>2</sup>
<b>Athlon MP2800+</b> <i>Barton</i> A2 (6-A-0) 05/2003 OEM: AMSN2800DUT4C	2133MHz	133MHz DDR	16.0x	1.6V	L1 128KB L2 512KB	37.5A 60.0W 90.0°C	54.3M 130nm 101mm <sup>2</sup>

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## Mobile Duron (SocketA)

Implementation Spitfire: 462pins CPGA - SocketA  
 Implementation Camaro: 462pins OPGA - SocketA  
 Instruction set Spitfire: RISC - IA32 - MMX - Ext. 3DNow!  
 Instruction set Camaro: RISC - IA32 - MMX - Ext. 3DNow! - SSE  
[\[info\]](#) Mobile AMD Duron Processor Model 7 datasheet (local backup)

CPU info		settings				cache			elec (max)			physics		
name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Mobile Duron 600</b> <i>Spitfire</i> A2 (6-3-1) 01/2001		600MHz	100MHz DDR	6.0x	?	L1 128KB L2 64KB				?			25.0M 180nm 100mm <sup>2</sup>	
<b>Mobile Duron 700</b> <i>Spitfire</i> A2 (6-3-1) 01/2001		700MHz	100MHz DDR	7.0x	?	L1 128KB L2 64KB				?			25.0M 180nm 100mm <sup>2</sup>	
<b>Mobile Duron 800</b> <i>Camaro</i> AO (6-7-0) 05/2001 OEM: DHM0800ALS1B		300~800MHz <i>PowerNow!</i>	100MHz DDR	3.0~8.0x	1.2~1.5V	L1 128KB L2 64KB				0.8~16.7A 25.0W 95.0°C			25.2M 180nm 106mm <sup>2</sup>	
<b>Mobile Duron 850</b> <i>Camaro</i> AO (6-7-0) 05/2001 OEM: DHM0850ALS1B		300~850MHz <i>PowerNow!</i>	100MHz DDR	3.0~8.5x	1.2~1.5V	L1 128KB L2 64KB				0.8~16.7A 25.0W 95.0°C			25.2M 180nm 106mm <sup>2</sup>	
<b>Mobile Duron 900</b> <i>Camaro</i> AO (6-7-0) 08/2001 OEM: DHM0900AQS1B		300~900MHz <i>PowerNow!</i>	100MHz DDR	3.0~9.0x	1.2~1.45V	L1 128KB L2 64KB				0.8~17.2A 25.0W 95.0°C			25.2M 180nm 106mm <sup>2</sup>	
<b>Mobile Duron 950</b> <i>Camaro</i> AO (6-7-0) 11/2001 OEM: DHM0950AQS1B		300~950MHz <i>PowerNow!</i>	100MHz DDR	3.0~9.5x	1.2~1.45V	L1 128KB L2 64KB				0.8~17.2A 25.0W 95.0°C			25.2M 180nm 106mm <sup>2</sup>	
<b>Mobile Duron 1000</b> <i>Camaro</i> AO (6-7-0) 12/2001 OEM: DHM1000ALS1B		300~1000MHz <i>PowerNow!</i>	100MHz DDR	3.0~10.0x	1.2~1.4V	L1 128KB L2 64KB				0.8~17.9A 25.0W 95.0°C			25.2M 180nm 106mm <sup>2</sup>	
<b>Mobile Duron 1100</b> <i>Camaro</i> AO (6-7-0) 01/2002 OEM: DHM1100ALS1B		300~1100MHz <i>PowerNow!</i>	100MHz DDR	3.0~11.0x	1.2~1.4V	L1 128KB L2 64KB				0.8~17.9A 25.0W 95.0°C			25.2M 180nm 106mm <sup>2</sup>	
<b>Mobile Duron 1200</b> <i>Camaro</i> AO (6-7-0) 01/2002 OEM: DHM1200ALS1B		300~1200MHz <i>PowerNow!</i>	100MHz DDR	3.0~12.0x	1.2~1.4V	L1 128KB L2 64KB				0.8~17.9A 25.0W 95.0°C			25.2M 180nm 106mm <sup>2</sup>	

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## Mobile Athlon (SocketA)

Implementation Corvette: 462pins CPGA - SocketA  
 Instruction set Corvette: RISC - IA32 - MMX - Ext. 3DNow! - SSE  
[\[info\]](#) 24319 - Mobile AMD Athlon 4 Processor Model 6 CPGA Data Sheet (local backup)

CPU info		settings				cache			elec (max)			physics		
name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Mobile Athlon 850</b> <i>Corvette</i> AO (6-6-0) 05/2001 OEM: AHM0850AVS3B		300~850MHz <i>PowerNow!</i>	100MHz DDR	3.0~8.5x	1.2~1.4V	L1 128KB L2 256KB				0.8~15.71A 22.0W 95.0°C			37.5M 180nm 130mm <sup>2</sup>	
<b>Mobile Athlon 900</b> <i>Corvette</i> AO (6-6-0) 05/2001 OEM: AHM0900AVS3B		300~900MHz <i>PowerNow!</i>	100MHz DDR	3.0~9.0x	1.2~1.4V	L1 128KB L2 256KB				0.8~17.14A 24.0W 95.0°C			37.5M 180nm 130mm <sup>2</sup>	
<b>Mobile Athlon 950</b> <i>Corvette</i> AO (6-6-0) 05/2001		300~950MHz <i>PowerNow!</i>	100MHz DDR	3.0~9.5x	1.2~1.4V	L1 128KB L2 256KB				0.8~17.14A 24.0W			37.5M 180nm	

OEM: AHM0950AVS3B							95.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1000</b>								
<i>Corvette</i>								
AO (6-6-0) 05/2001	300~1000MHz	100MHz	3.0~10.0x	1.2~1.4V	L1 128KB	0.8~17.9A	37.5M	
OEM: AHM1000AVS3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	25.0W	180nm	
							95.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1000 DTR</b>								
<i>Corvette</i>								
AO (6-6-0) ??/2001	300~1000MHz	100MHz	3.0~10.0x	1.6V	L1 128KB	21.9A	37.5M	
OEM: AHM1000AUQ3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	35.0W	180nm	
							100.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1100</b>								
<i>Corvette</i>								
AO (6-6-0) 08/2001	300~1100MHz	100MHz	3.0~11.0x	1.2~1.4V	L1 128KB	0.8~17.9A	37.5M	
OEM: AHM1100AVS3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	25.0W	180nm	
							95.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1100 DTR</b>								
<i>Corvette</i>								
AO (6-6-0) ??/2001	300~1100MHz	100MHz	3.0~11.0x	1.6V	L1 128KB	21.9A	37.5M	
OEM: AHM1100AUQ3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	35.0W	180nm	
							100.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1200</b>								
<i>Corvette</i>								
AO (6-6-0) 11/2001	300~1200MHz	100MHz	3.0~12.0x	1.2~1.35V	L1 128KB	0.8~18.5A	37.5M	
OEM: AHM1200AJS3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	25.0W	180nm	
							95.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1200 DTR</b>								
<i>Corvette</i>								
AO (6-6-0) ??/2001	300~1200MHz	100MHz	3.0~12.0x	1.55V	L1 128KB	22.6A	37.5M	
OEM: AHM1200AHQ3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	35.0W	180nm	
							100.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1500+ DTR</b>								
<i>Corvette</i>								
AO (6-6-0) ??/2001	300~1300MHz	100MHz	3.0~13.0x	1.5V	L1 128KB	23.3A	37.5M	
OEM: AHM1500ALQ3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	35.0W	180nm	
							100.0°C	130mm <sup>2</sup>
<b>Mobile Athlon 1600+ DTR</b>								
<i>Corvette</i>								
AO (6-6-0) ??/2001	300~1400MHz	100MHz	3.0~13.0x	1.45V	L1 128KB	24.1A	37.5M	
OEM: AHM1600AQQ3B	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	35.0W	180nm	
							100.0°C	130mm <sup>2</sup>

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## Mobile Athlon XP-M (SocketA)

Implementation Palomino/Thoroughbred/Barton: 462pins OPGA - SocketA

Instruction set Palomino/Thoroughbred/Barton: RISC - IA32 - MMX - Ext. 3DNow! - SSE

	CPU info		settings			cache			elec (max)			physics			
	name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D
<b>Mobile XP-M 1400+</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 04/2002			533~1200MHz	133MHz	4.0~10.0x	1.45V	L1 128KB				24.1A		37.2M		
OEM: AXMD1400FQQ3C			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				35.0W		130nm		
										100.0°C			81mm <sup>2</sup>		
<b>Mobile XP-M 1400+ DTR 45W</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 09/2002			533~1200MHz	133MHz	4.0~9.0x	1.45V	L1 128KB				31.0A		37.2M		
BO (6-8-1) 11/2002			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				45.0W		130nm		
OEM: AXMH1400FQQ3C											100.0°C		81mm <sup>2</sup>		
<b>Mobile XP-M 1500+</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 04/2002			533~1333MHz	133MHz	4.0~10.0x	1.45V	L1 128KB				24.1A		37.2M		
OEM: AXMD1500FQQ3C			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				35.0W		130nm		
										100.0°C			81mm <sup>2</sup>		
<b>Mobile XP-M 1500+ DTR 45W</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 09/2002			533~1333MHz	133MHz	4.0~10.0x	1.45V	L1 128KB				31.0A		37.2M		
BO (6-8-1) 11/2002			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				45.0W		130nm		
OEM: AXMH1500FQQ3C											100.0°C		81mm <sup>2</sup>		
<b>Mobile XP-M 1600+</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 04/2002			533~1400MHz	133MHz	4.0~10.5x	1.45V	L1 128KB				24.1A		37.2M		
OEM: AXMD1600FQQ3C			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				35.0W		130nm		
										100.0°C			81mm <sup>2</sup>		
<b>Mobile XP-M 1600+ DTR 45W</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 09/2002			533~1400MHz	133MHz	4.0~10.5x	1.55/1.45V	L1 128KB				29.0/31.0A		37.2M		
BO (6-8-1) 11/2002			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				45.0W		130nm		
OEM: AXMH1600FHQ3C (AO)											100.0°C		81mm <sup>2</sup>		
OEM: AXMH1600FQQ3C (BO)															
<b>Mobile XP-M 1700+</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 04/2002			533~1466MHz	133MHz	4.0~11.0x	1.45V	L1 128KB				24.1A		37.2M		
OEM: AXMD1700FQQ3C			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				35.0W		130nm		
										100.0°C			81mm <sup>2</sup>		
<b>Mobile XP-M 1700+ DTR 45W</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 09/2002			533~1466MHz	133MHz	4.0~11.0x	1.55/1.45V	L1 128KB				29.0/31.0A		37.2M		
BO (6-8-1) 11/2002			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				45.0W		130nm		
OEM: AXMH1700FHQ3C (AO)											100.0°C		81mm <sup>2</sup>		
OEM: AXMH1700FQQ3C (BO)															
<b>Mobile XP-M 1800+</b>															
<i>Thoroughbred</i>															
AO (6-8-0) 07/2002			533~1533MHz	133MHz	4.0~11.5x	1.4V	L1 128KB				25.0A		37.2M		
OEM: AXMD1800FVQ3C			<i>PowerNow!</i>	<i>DDR</i>			L2 256KB				35.0W		130nm		
										100.0°C			81mm <sup>2</sup>		

**Mobile XP-M 1800+ DTR 45W***Thoroughbred*

A0 (6-8-0) 09/2002

BO (6-8-1) 11/2002

OEM: AXMH1800FHQ3C (A0)

OEM: AXMH1800FQQ3C (BO)

533~1533MHz

*PowerNow!*

133MHz

*DDR*

4.0~11.5x

1.55/1.45V

L1 128KB

L2 256KB

29.0/31.0A

45.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 1800+ LP 35W***Thoroughbred*

BO (6-8-1) 11/2002

OEM: AXMD1800GJQ3C

533~1533MHz

*PowerNow!*

133MHz

*DDR*

4.0~11.5x

1.35V

L1 128KB

L2 256KB

25.9A

35.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 1900+***Thoroughbred*

A0 (6-8-0) 09/2002

OEM: AXMD1900FVQ3C

533~1600MHz

*PowerNow!*

133MHz

*DDR*

4.0~12.0x

1.4V

L1 128KB

L2 256KB

25.0A

35.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 1900+ DTR 45W***Thoroughbred*

A0 (6-8-0) 09/2002

BO (6-8-1) 11/2002

OEM: AXMH1900FLQ3C (A0)

OEM: AXMH1900FQQ3C (BO)

533~1600MHz

*PowerNow!*

133MHz

*DDR*

4.0~12.0x

1.5/1.45V

L1 128KB

L2 256KB

30.0/31.0A

45.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 1900+ LP 35W***Thoroughbred*

BO (6-8-1) 11/2002

OEM: AXMD1900GJQ3C

533~1600MHz

*PowerNow!*

133MHz

*DDR*

4.0~12.0x

1.35V

L1 128KB

L2 256KB

25.9A

35.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 2000+ DTR 45W***Thoroughbred*

A0 (6-8-0) 09/2002

BO (6-8-1) 11/2002

OEM: AXMH2000FLQ3C (A0)

OEM: AXMH2000FQQ3C (BO)

533~1666MHz

*PowerNow!*

133MHz

*DDR*

4.0~12.5x

1.5/1.45V

L1 128KB

L2 256KB

30.0/31.0A

45.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 2000+ DTR 72W***Thoroughbred*

BO (6-8-1) 11/2002

OEM: AXMA2000FUT4C

533~1666MHz

*PowerNow!*

133MHz

*DDR*

4.0~12.5x

1.6V

L1 128KB

L2 256KB

38.3A

61.3W

90.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 2000+ LP 35W***Barton*

A2 (6-A-0) 03/2003

OEM: AXMD2000GJQ4C

800~1533MHz

*PowerNow!*

133MHz

*DDR*

6.0~11.5x

1.35V

L1 128KB

L2 512KB

25.9A

35.0W

100.0°C

54.3M

130nm

101mm<sup>2</sup>**Mobile XP-M 2000+ LP 35W***Thoroughbred*

BO (6-8-1) ??/2002

OEM: AXMD2000GJQ3C

533~1666MHz

*PowerNow!*

133MHz

*DDR*

4.0~12.5x

1.35V

L1 128KB

L2 256KB

25.9A

35.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 2200+ DTR 45W***Thoroughbred*

BO (6-8-1) 11/2002

OEM: AXMH2200FQQ3C

533~1800MHz

*PowerNow!*

133MHz

*DDR*

4.0~13.5x

1.45V

L1 128KB

L2 256KB

31.0A

45.0W

100.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 2200+ DTR 72W***Thoroughbred*

BO (6-8-1) 11/2002

OEM: AXMA2200FUT4C

533~1800MHz

*PowerNow!*

133MHz

*DDR*

4.0~13.5x

1.6V

L1 128KB

L2 256KB

39.3A

62.8W

90.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 2200+ LP 35W***Barton*

A2 (6-A-0) 03/2003

OEM: AXMD2200FJQ4C

800~1666MHz

*PowerNow!*

133MHz

*DDR*

6.0~12.5x

1.35V

L1 128KB

L2 512KB

25.9A

35.0W

100.0°C

54.3M

130nm

101mm<sup>2</sup>**Mobile XP-M 2400+***Barton*

A2 (6-A-0) 03/2003

OEM: AXMH2400FQQ4C

800~1800MHz

*PowerNow!*

133MHz

*DDR*

6.0~13.5x

1.45V

L1 128KB

L2 512KB

31.0A

45.0W

100.0°C

54.3M

130nm

101mm<sup>2</sup>**Mobile XP-M 2400+ DTR 72W***Barton*

A2 (6-A-0) 03/2003

OEM: AXMA2400FUT4C

800~1800MHz

*PowerNow!*

133MHz

*DDR*

6.0~13.5x

1.6V

L1 128KB

L2 512KB

39.3A

62.8W

90.0°C

54.3M

130nm

101mm<sup>2</sup>**Mobile XP-M 2400+ DTR 72W***Thoroughbred*

BO (6-8-1) 11/2002

OEM: AXMA2400FKT4C

533~2000MHz

*PowerNow!*

133MHz

*DDR*

4.0~15.0x

1.65V

L1 128KB

L2 256KB

41.4A

68.3W

90.0°C

37.2M

130nm

81mm<sup>2</sup>**Mobile XP-M 2400+ LP 35W***Barton*

A2 (6-A-0) 03/2003

OEM: AXMD2400FJQ4C

800~1800MHz

*PowerNow!*

133MHz

*DDR*

6.0~13.5x

1.35V

L1 128KB

L2 512KB

25.9A

35.0W

100.0°C

54.3M

130nm

101mm<sup>2</sup>**Mobile XP-M 2500+***Barton*

A2 (6-A-0) 03/2003

OEM: AXMH2500FQQ4C

800~1866MHz

*PowerNow!*

133MHz

*DDR*

6.0~14.0x

1.45V

L1 128KB

L2 512KB

31.0A

45.0W

100.0°C

54.3M

130nm

101mm<sup>2</sup>**Mobile XP-M 2500+ DTR 72W***Barton*

A2 (6-A-0) 03/2003

OEM: AXMA2500FKT4C

800~1866MHz

*PowerNow!*

133MHz

*DDR*

6.0~14.0x

1.65V

L1 128KB

L2 512KB

41.4A

68.3W

**Mobile XP-M 2600+ DTR 72W***Thoroughbred*

BO (6-8-1) 11/2002

OEM: AXMA2600FKT4C

533~2133MHz  
*PowerNow!*133MHz  
*DDR*

4.0~16.0x

1.65V

L1 128KB  
L2 256KB41.4A  
68.3W  
90.0°C37.2M  
130nm  
81mm<sup>2</sup>**Mobile XP-M 2800+ DTR 53W***Barton*

A2 (6-A-0) Q4/2004

OEM: AXMJ2800FHQ4C

800~2133MHz  
*PowerNow!*133MHz  
*DDR*

6.0~16.0x

1.55V

L1 128KB  
L2 512KB41.4A  
53.0W  
90.0°C54.3M  
130nm  
101mm<sup>2</sup>**Mobile XP-M 2800+ DTR 72W***Barton*

A2 (6-A-0) 03/2003

OEM: AXMA2800FKT4C

800~2133MHz  
*PowerNow!*133MHz  
*DDR*

6.0~16.0x

1.65V

L1 128KB  
L2 512KB41.4A  
68.3W  
90.0°C54.3M  
130nm  
101mm<sup>2</sup>**Mobile XP-M 3000+ DTR 72W***Barton*

A2 (6-A-0) 03/2003

OEM: AXMA3000FKT4C

800~2200MHz  
*PowerNow!*133MHz  
*DDR*

6.0~16.5x

1.65V

L1 128KB  
L2 512KB41.4A  
68.3W  
90.0°C54.3M  
130nm  
101mm<sup>2</sup>

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**Mobile Athlon XP-M Low Power (Socket563)**

Implementation Uitvoering Thoroughbred/Barton: 563pins OuPGA - Socket563

Instruction set Thoroughbred/Barton: RISC - IA32 - MMX - Ext. 3DNow! - SSE

CPU info			settings				cache			elec (max)			physics		
name	release	speed	FSB	MP	VCore	L1	L2	L3	I	P	T	T	P	D	
<b>Mobile XP-M 1200+ LP 16W</b>															
<i>Thoroughbred</i>		400~1000MHz	100MHz	4.0~10.0x	1.2V	L1 128KB	L2 256KB		13.3A	16.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
OEM: AXML1200GTS3B															
<b>Mobile XP-M 1300+ LP 16W</b>															
<i>Thoroughbred</i>		400~1100MHz	100MHz	4.0~11.0x	1.15/1.1V	L1 128KB	L2 256KB		13.9/14.5A	16.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
BO (6-8-1) 11/2002															
OEM: AXML1300GCS3B (A0)															
OEM: AXML1300GYS3B (BO)															
<b>Mobile XP-M 1400+ LP 16W (200)</b>															
<i>Thoroughbred</i>		400~1200MHz	100MHz	4.0~12.0x	1.1V	L1 128KB	L2 256KB		14.5A	16.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
BO (6-8-1) 11/2002		<i>PowerNow!</i>	<i>DDR</i>												
OEM: AXML1400GYS3B															
<b>Mobile XP-M 1400+ LP 16W (266)</b>															
<i>Thoroughbred</i>		533~1200MHz	133MHz	4.0~9.0x	1.1V	L1 128KB	L2 256KB		14.5A	16.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
BO (6-8-1) 11/2002		<i>PowerNow!</i>	<i>DDR</i>												
OEM: AXML1400GYS3C															
<b>Mobile XP-M 1400+ LP 25W (200)</b>															
<i>Thoroughbred</i>		400~1200MHz	100MHz	4.0~12.0x	1.3/1.25V	L1 128KB	L2 256KB		19.2/20.0A	25.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
BO (6-8-1) 11/2002															
OEM: AXMS1400GWS3B (A0)															
OEM: AXMS1400GXS3B (BO)															
<b>Mobile XP-M 1400+ LP 25W (266)</b>															
<i>Thoroughbred</i>		533~1200MHz	133MHz	4.0~9.0x	1.3/1.25V	L1 128KB	L2 256KB		19.2/20.0A	25.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
BO (6-8-1) 11/2002															
OEM: AXMS1400GWS3C (A0)															
OEM: AXMS1400GXS3C (BO)															
<b>Mobile XP-M 1500+ LP 16W</b>															
<i>Thoroughbred</i>		400~1300MHz	100MHz	4.0~13.0x	1.1V	L1 128KB	L2 256KB		14.5A	16.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
BO (6-8-1) 11/2002		<i>PowerNow!</i>	<i>DDR</i>												
OEM: AXML1500GYS3B															
<b>Mobile XP-M 1500+ LP 25W (200)</b>															
<i>Thoroughbred</i>		400~1300MHz	100MHz	4.0~13.0x	1.3/1.25V	L1 128KB	L2 256KB		19.2/20.0A	25.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
BO (6-8-1) 11/2002															
OEM: AXMS1500GWS3B (A0)															
OEM: AXMS1500GXS3B (BO)															
<b>Mobile XP-M 1500+ LP 25W (266)</b>															
<i>Thoroughbred</i>		533~1333MHz	133MHz	4.0~10.0x	1.3/1.25V	L1 128KB	L2 256KB		19.2/20.0A	25.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
BO (6-8-1) 11/2002															
OEM: AXMS1500GWS3C (A0)															
OEM: AXMS1500GXS3C (BO)															
<b>Mobile XP-M 1600+ LP 25W (200)</b>															
<i>Thoroughbred</i>		400~1400MHz	100MHz	4.0~14.0x	1.3/1.25V	L1 128KB	L2 256KB		19.2/20.0A	25.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
BO (6-8-1) 11/2002															
OEM: AXMS1600GWS3B (A0)															
OEM: AXMS1600GXS3B (BO)															
<b>Mobile XP-M 1600+ LP 25W (266)</b>															
<i>Thoroughbred</i>		533~1400MHz	133MHz	4.0~10.5x	1.3/1.25V	L1 128KB	L2 256KB		19.2/20.0A	25.0W	95.0°C	37.2M	130nm	81mm <sup>2</sup>	
A0 (6-8-0) 09/2002		<i>PowerNow!</i>	<i>DDR</i>												
BO (6-8-1) 11/2002															
OEM: AXMS1600GWS3C (A0)															
OEM: AXMS1600GXS3C (BO)															
<b>Mobile XP-M 1700+ LP 16W</b>															
<i>Barton</i>		400~1300MHz	100MHz	4.0~13.0x	1.1V	L1 128KB	L2 512KB		14.5A	16.0W	95.0°C	54.3M	130nm	101mm <sup>2</sup>	
A2 (6-A-0) 03/2003		<i>PowerNow!</i>	<i>DDR</i>												
OEM: AXML1700GYS4B															

Mobile XP-M 1700+ LP 25W	533~1466MHz	133MHz	4.0~11.0x	1.25V	Fast menu	25.0W	130nm
<i>Thoroughbred</i> BO (6-8-1) 11/2002 OEM: AXMS1700GXS3C	<i>PowerNow!</i>	<i>DDR</i>			L2 256KB	95.0°C	81mm <sup>2</sup>
<b>Mobile XP-M 1800+ LP 16W</b>	400~1400MHz	100MHz	4.0~14.0x	1.1V	L1 128KB L2 512KB	14.5A 16.0W 95.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXML1800GYS4B	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 1800+ LP 25W</b>	533~1533MHz	133MHz	4.0~11.5x	1.25V	L1 128KB L2 256KB	20.0A 25.0W 95.0°C	37.2M 130nm 81mm <sup>2</sup>
<i>Thoroughbred</i> BO (6-8-1) 11/2002 OEM: AXMS1800GXS3C	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 1800+ LP 25W (200)</b>	400~1400MHz	100MHz	4.0~14.0x	1.25V	L1 128KB L2 512KB	20.0A 25.0W 95.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMS1800GXS4B	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 1800+ LP 25W (266)</b>	533~1400MHz	133MHz	4.0~10.5x	1.25V	L1 128KB L2 512KB	20.0A 25.0W 95.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMS1800GXS4C	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 1900+ LP 25W</b>	533~1466MHz	133MHz	4.0~11.0x	1.25V	L1 128KB L2 512KB	20.0A 25.0W 95.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMS1900GXS4C	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 2000+ LP 25W</b>	533~1533MHz	133MHz	4.0~11.5x	1.25V	L1 128KB L2 512KB	20.0A 25.0W 95.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMS2000GXS4C	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 2100+ LP 25W (200)</b>	400~1600MHz	100MHz	4.0~16.0x	1.25V	L1 128KB L2 512KB	20.0A 25.0W 95.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMS2100GXS4B	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 2100+ LP 25W (266)</b>	533~1600MHz	133MHz	4.0~12.0x	1.25V	L1 128KB L2 512KB	20.0A 25.0W 95.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMS2100GXS4C	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 2200+ LP 35W</b>	800~1666MHz	133MHz	6.0~12.5x	1.35V	L1 128KB L2 512KB	25.9A 35.0W 100.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMD2200GJQ4C	<i>PowerNow!</i>	<i>DDR</i>					
<b>Mobile XP-M 2400+ LP 35W</b>	800~1800MHz	133MHz	6.0~13.5x	1.35V	L1 128KB L2 512KB	25.9A 35.0W 100.0°C	54.3M 130nm 101mm <sup>2</sup>
<i>Barton</i> A2 (6-A-0) 03/2003 OEM: AXMD2400GJQ4C	<i>PowerNow!</i>	<i>DDR</i>					

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## Remarks

CPU

remark

**Code** AAAAnnnnPVT**CF**

### CPU model

A = Athlon (Thunderbird)  
 AHX = Athlon MP (Palomino 1000 and 1200)  
 AMP = Athlon MP (Palomino)  
 AMSN = Athlon MP (Thoroughbred)  
 AX = Athlon XP (Thunderbird/Palomino)  
 AXD = Athlon XP (Thoroughbred/Barton/Thornton)  
 AXDA = Athlon XP (Thoroughbred/Barton)  
 AXMA = Mobile Athlon XP max 72W (Mobile Thoroughbred/Mobile Barton)  
 AXMD = Mobile Athlon XP max 35W (Mobile Thoroughbred/Mobile Barton)  
 AXMG = Mobile Athlon XP max 47W (Mobile Barton)  
 AXMH = Mobile Athlon XP max 45W (Mobile Thoroughbred/Mobile Barton)  
 AXMJ = Mobile Athlon XP max 53W (Mobile Barton)  
 AXML = Mobile Athlon XP max 16W (Mobile Thoroughbred/Mobile Barton)  
 AXMS = Mobile Athlon XP max 25W (Mobile Thoroughbred/Mobile Barton)  
 D = Duron (Spitfire)  
 DHD = Duron (Morgan/Applebred)  
 DHM = Mobile Duron (Camaro)  
 K7 = Athlon (Argon/Pluto/Orion)  
 SDA = Sempron (Thornton)

**AAAA**

### CPU speed/model rating

Only the older non-XP models (K7/K75 Model 1 and 2) have 3 digits in here and the Athlon 1000 (Orion) is indicated by "100". The newer XP models are indicated by 4-digit Model Ratings.

### CPU package

A = CPGA (Spitfire/Thunderbird/Palomino MP (6-6-0)/Camaro)  
 D = OPGA (Morgan/Applebred/Palomino/Thoroughbred/Thornton/Barton)  
 P = OPGA (Mobile Thoroughbred/Mobile Barton)  
 G = OuPGA (Mobile Thoroughbred/Mobile Barton)  
 M = Module (Argon/Pluto/Orion)

### CPU core voltage

C = 1,15V (Mobile Thoroughbred)

H = 1,55V (Mobile Barton)  
 J = 1,35V (Mobile Barton)  
 K = 1,65V (Thoroughbred/Thorton/Barton)  
 L = 1,50V (Spitfire/Applebred/Thoroughbred)  
 M = 1,75V (Morgan/Thunderbird/Palomino)  
 P = 1,70V (Thunderbird)  
 Q = 1,45V (Camaro/Mobile Thoroughbred/Mobile Barton)  
 S = 1,50V (Spitfire/Thoroughbred)  
 T = 1,20V (Mobile Thoroughbred)  
 U = 1,60V (Spitfire/Thoroughbred/Thorton/Barton)  
 V = 1,40V (Camaro)  
 W = 1,30V (Mobile Thoroughbred)  
 X = 1,25V (Mobile Thoroughbred/Mobile Barton)  
 Y = 1,10V (Mobile Thoroughbred/Mobile Barton)

V

**maximum temperature**

Q = 100°C (Mobile Thoroughbred/Mobile Barton)  
 S = 95°C (Thunderbird/Camaro)  
 T = 90°C (Spitfire/Morgan/Applebred/Thunderbird/Palomino/Thoroughbred/Thorton/Barton)  
 V = 85°C (Applebred/Thoroughbred/Barton)  
 Z = 85°C (Thoroughbred/Thorton/Barton)

T

K7 decoding

**L2 cache size**

1 = 64KB (Spitfire/Morgan/Applebred/Camaro)  
 3 = 256KB (Thunderbird/Palomino/Thoroughbred/Thorton/Mobile Thoroughbred)  
 4 = 512KB (Barton/Mobile Barton)

C

**2<sup>nd</sup> digit: L2 cache divider (SlotA CPU's only)**

1 = 2:1 (Athlon 500-700)  
 2 = 2:5 (Athlon 750-850)  
 3 = 3:1 (Athlon 900-1000)  
 4 = 1:1 (Athlon Thunderbird)

**FSB speed**

A = 100MHz DDR (Argon/Pluto/Orion)  
 B = 100MHz DDR (Spitfire/Morgan/Thunderbird/Camaro/Corvette/Mobile Palomino)  
 C = 133MHz DDR (Applebred/Thunderbird/Palomino/Thoroughbred/Thorton/Barton/Mobile Palomino/Mobile Thoroughbred/Mobile Barton)  
 D = 166MHz DDR (Thoroughbred/Thorton/Barton)  
 E = 200MHz DDR (Barton)

For example: **AXDA2600DKV3D** = Athlon XP2600+ Thoroughbred-B

Thunderbird/Palomino/Spitfire/Morgan: connect all L1 bridges to unlock the multiplier. On the ceramic Thunderbird/Spitfire models, you could do this easily by using a pencil. On the plastic Palomino/Morgan you have to fill in the lasered holes using rapid glue and then connect the bridges by electrolytic silver paint.

Thoroughbred: connect the last L3 bridge to unlock the multiplier.

K7 L-bridges

Palomino/Thoroughbred: connect the last L5 bridge to convert Athlon XP to Athlon MP.

Applebred/Thorton: connect all L2 bridges to convert to Thoroughbred/Barton respectively **and** to double the L2 cache. Caution: stability is not guaranteed.

Also see [AMDBoard.com](http://AMDBoard.com) - [Overclocking specials](#).

K7 SMP

The Athlon MP is officially tested to be capable of running in SMP. The 'single processor' Athlon (XP) isn't officially tested, but in fact the XP's can also do Dual CPU, however stability is not guaranteed.

Socket A

This socket is specified as a 462 socket, but of the 462 pins there are 9 pins closed. This is to prevent inserting Socket370 Intel CPU's on it. So in fact, all Socket A CPU's are 453 pins. Count it :o)

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**Request** - Found an error? Have something new? Be gentle to send a [mail](#).

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